

INTER-OFFICE MEMORANDUM

April 28, 1978

From: F. Gruppuso

To : K. Greenberg

cc : S. Maine T. Mariner S. Stuart
 D. Harrower R. Curth I. Harris
 R. Stephens R. Werner S. Kelly
 F. Jelenko P. Rush J. Wunner

Re : CP1600 INSTRUCTION SET

Please note that due to an oversight in generating the original CP1600 Documentation Manual that the list of uninterruptable instructions was and still is incomplete.

As this memo is intended to constitute an official change, it should be included in the next release of that documentation (sometime within the next decade would be preferable).

All combinations of the MVO instructions are uninterruptable. This includes direct, indirect and immediate addressing modes. Uninterruptable in this context means the CPU will not recognize low active enabled signals on the BUSRQ*, INTR* or INTRM* lines for any reason but rather will continue on and execute the next instruction.

Thus, the set of uninterruptable instructions include all MVO, all SHIFTS, EIS, DIS, TCI, CLRC, SETC instructions.

The impact on TIC and STIC software should be carefully considered.

Frank

/do

LOGIC BOARD PARTS

7 89 chips

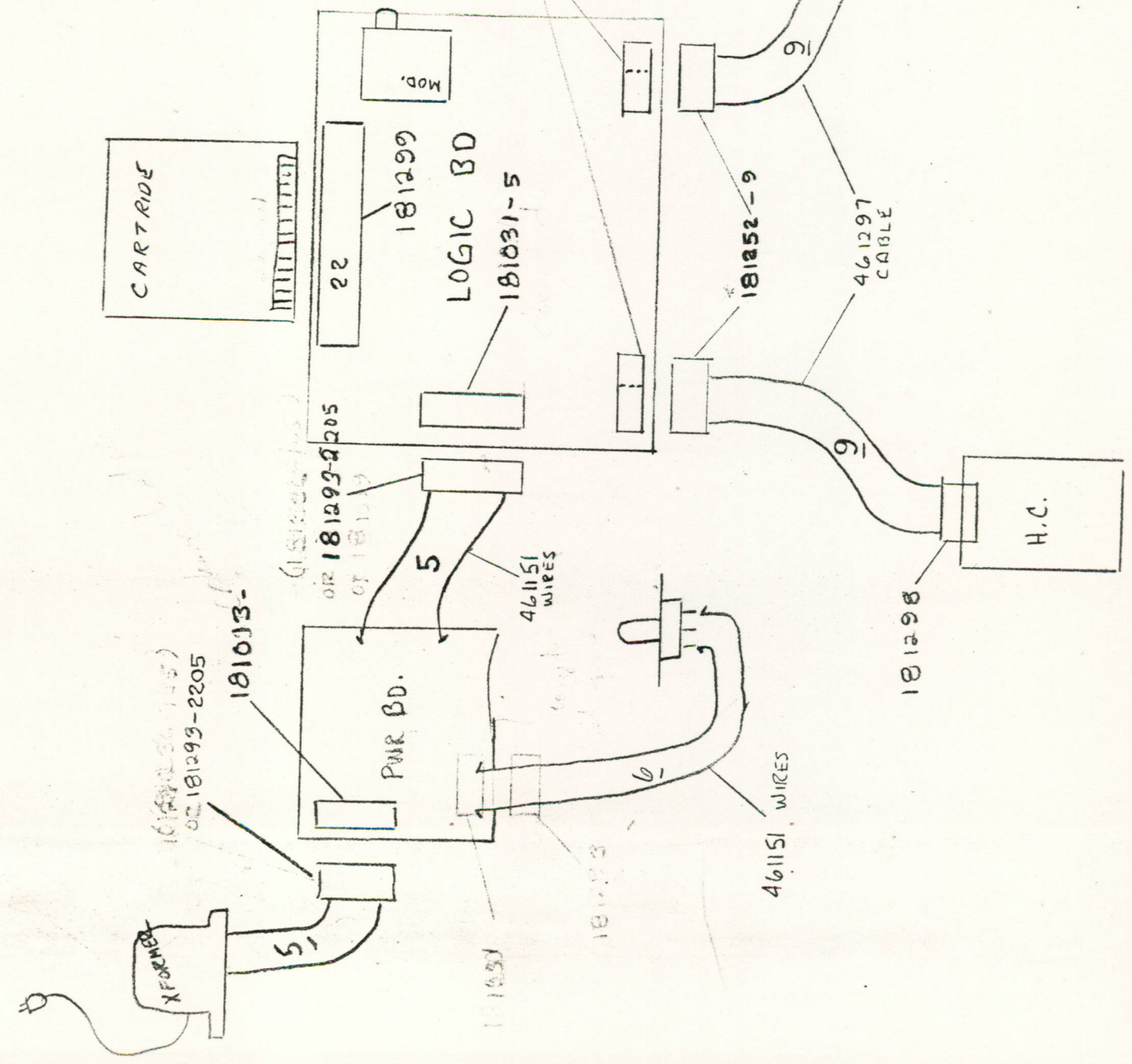
4 2112 RAMS

MATTEL 2609 SYSTEM INTERCONNECTS SHOWING MAGNIFY PART #'S

MOV #	VENDOR	WIRE SIZE	DESCRIPTION
181293	AMP	22GA	I.D.S. CONN. .156"
181299	TRW	.062" CABLE	20 PIN EDGE CARD
181252	AMP	28GA	I.D.S. CONN. .100"
181254	AMP	—	WAFER .100" RT L
181031	MOLEX	—	WAFER .156" RT L
181298	CIRCUIT ASSM CORP	28GA	I.D.S. CUSTOM
181210	MOLEX	18-24	.156" CRIMP TERMINAL
181013	MOLEX	—	WAFER .156" STRAIG
181236	IAMP	22GA	.156 LACE FLOC



NOTE: NOT TO SCALE
CONNECTOR LOCATIONS DO NOT REPRESENT ACTUAL LAYOUT POSITIONS.



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